



SEMIPACK® 3

## Thyristor Modules

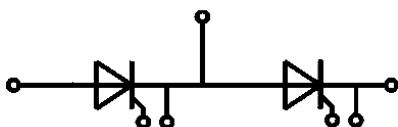
## SKKT 323/18 E

## Features\*

- Industrial standard package
- Electrically insulated base plate
- Heat transfer through aluminum oxide ceramic insulated metal base plate
- Chip soldered on direct copper bonded Al<sub>2</sub>O<sub>3</sub> ceramic
- UL recognition, file no. E63532

## Typical Applications

- DC motor control (e. g. for machine tools)
- Temperature control (e. g. for ovens, chemical processes)
- Professional light dimming (studios, theaters)



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## Absolute Maximum Ratings

Symbol	Conditions		Values	Unit
<b>Chip</b>				
$I_{T(AV)}$	sinus 180°	$T_c = 85\text{ °C}$	320	A
		$T_c = 100\text{ °C}$	241	A
$I_{TSM}$	10 ms	$T_j = 25\text{ °C}$	9500	A
		$T_j = 130\text{ °C}$	8200	A
$i^2t$	10 ms	$T_j = 25\text{ °C}$	451250	A <sup>2</sup> s
		$T_j = 130\text{ °C}$	336200	A <sup>2</sup> s
$V_{RSM}$			1900	V
$V_{RRM}$			1800	V
$V_{DRM}$			1800	V
$(di/dt)_{cr}$	$T_j = 130\text{ °C}$		130	A/μs
$(dv/dt)_{cr}$	$T_j = 130\text{ °C}$		1000	V/μs
$T_j$			-40 ... 130	°C
<b>Module</b>				
$T_{stg}$			-40 ... 125	°C
$V_{isol}$	a.c.; 50 Hz; r.m.s.	1 min	3000	V
		1 s	3600	V

## Characteristics

Symbol	Conditions		min.	typ.	max.	Unit
Chip						
V <sub>T</sub>	T <sub>j</sub> = 25 °C, I <sub>T</sub> = 750 A				1.45	V
V <sub>T(TO)</sub>	T <sub>j</sub> = 130 °C				0.81	V
r <sub>T</sub>	T <sub>j</sub> = 130 °C				0.85	mΩ
I <sub>DD</sub> ;I <sub>RD</sub>	T <sub>j</sub> = 130 °C, V <sub>DD</sub> = V <sub>DRM</sub> ; V <sub>RD</sub> = V <sub>RRM</sub>				100	mA
t <sub>gd</sub>	T <sub>j</sub> = 25 °C, I <sub>G</sub> = 1 A, di <sub>G</sub> /dt = 1 A/μs			1		μs
t <sub>gr</sub>	V <sub>D</sub> = 0.67 * V <sub>DRM</sub>			2		μs
t <sub>q</sub>	T <sub>j</sub> = 130 °C			150		μs
I <sub>H</sub>	T <sub>j</sub> = 25 °C			150	500	mA
I <sub>L</sub>	T <sub>j</sub> = 25 °C, R <sub>G</sub> = 33 Ω			300	2000	mA
V <sub>GT</sub>	T <sub>j</sub> = 25 °C, d.c.		2			V
I <sub>GT</sub>	T <sub>j</sub> = 25 °C, d.c.		150			mA
V <sub>GD</sub>	T <sub>j</sub> = 130 °C, d.c.				0.25	V
I <sub>GD</sub>	T <sub>j</sub> = 130 °C, d.c.				10	mA
R <sub>th(j-c)</sub>	cont.	per chip			0.091	K/W
		per module			0.0455	K/W
R <sub>th(j-c)</sub>	sin. 180°	per chip			0.095	K/W
		per module			0.048	K/W
R <sub>th(j-c)</sub>	rec. 120°	per chip			0.11	K/W
		per module			0.055	K/W
Module						
R <sub>th(c-s)</sub>	chip			0.08		K/W
	module			0.04		K/W
M <sub>s</sub>	to heatsink M5		4.25		5.75	Nm
M <sub>t</sub>	to terminals M8		7.65		10.35	Nm
a					5 * 9.81	m/s <sup>2</sup>
w				410		g

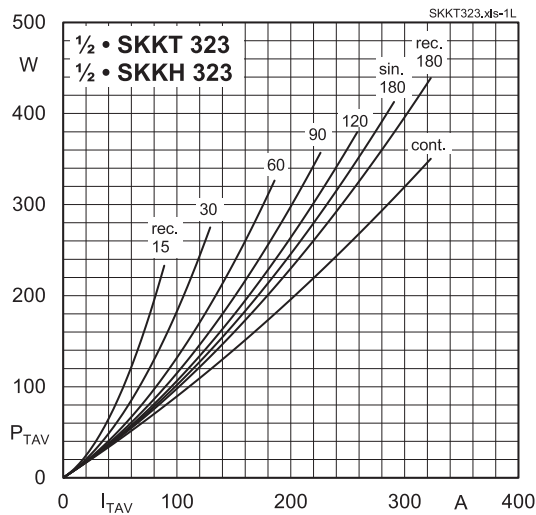


Fig. 1L: Power dissipation per thyristor/diode vs. on-state current

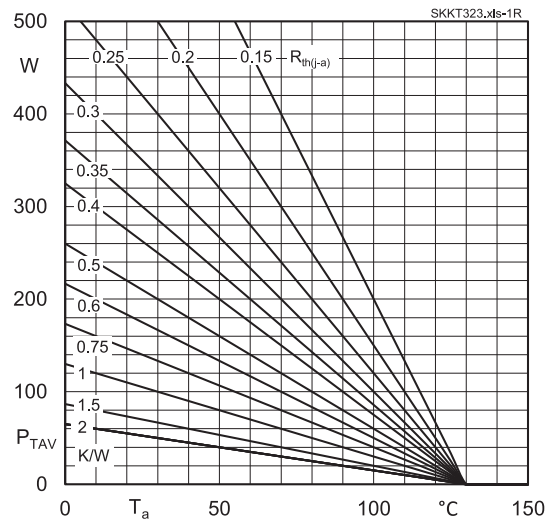


Fig. 1R: Power dissipation per thyristor/diode vs. ambient temperature

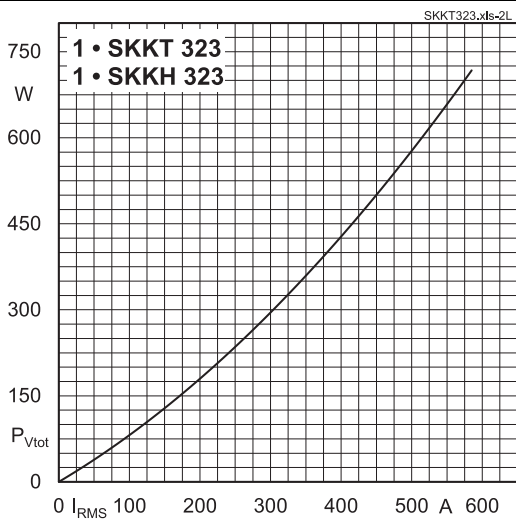


Fig. 2L: Power dissipation of one module vs. rms current

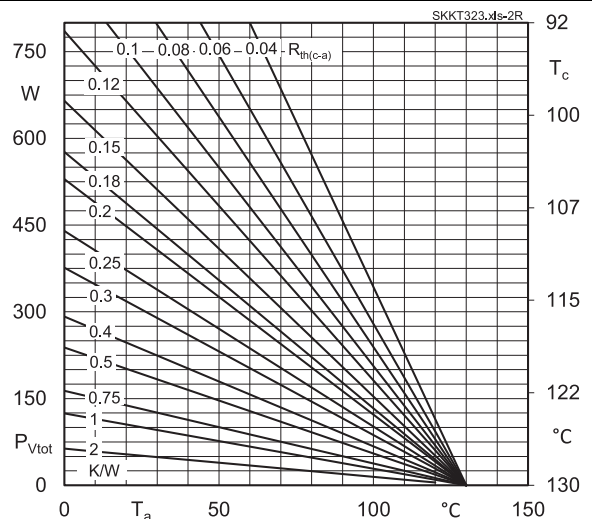


Fig. 2R: Power dissipation of one module vs. case temperature

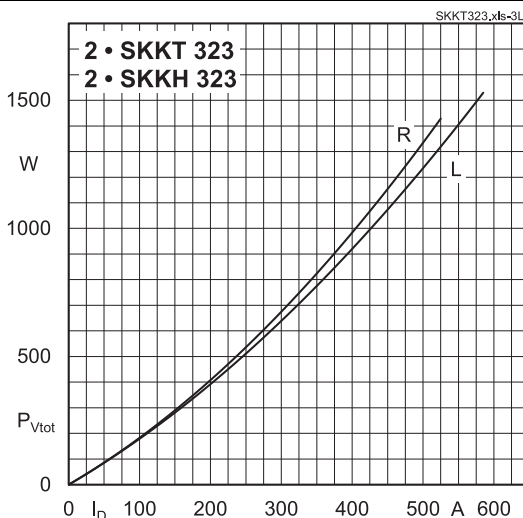


Fig. 3L: Power dissipation of two modules vs. direct current

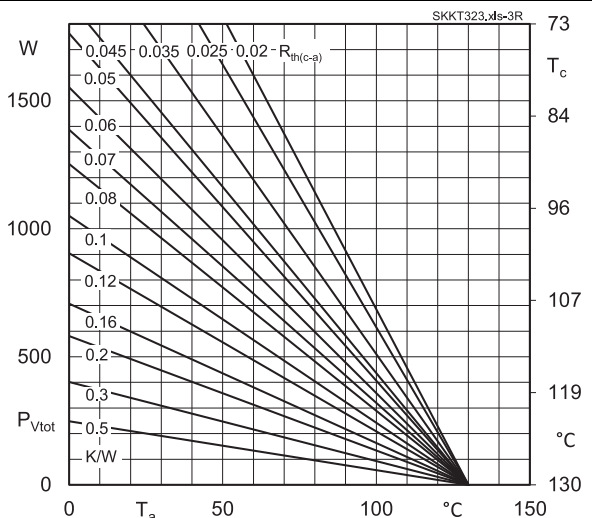


Fig. 3R: Power dissipation of two modules vs. case temperature

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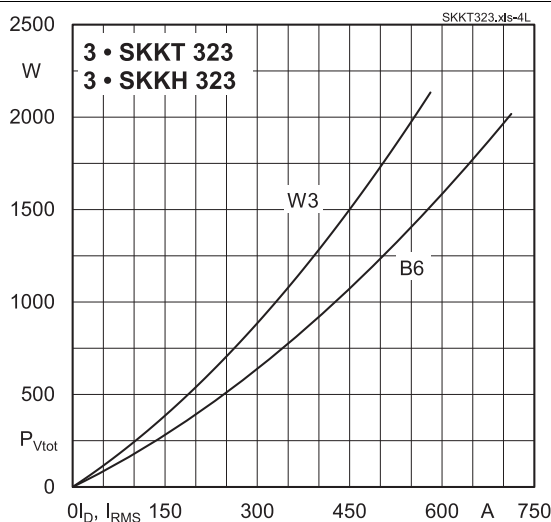


Fig. 4L: Power dissipation of three modules vs. direct and rms current

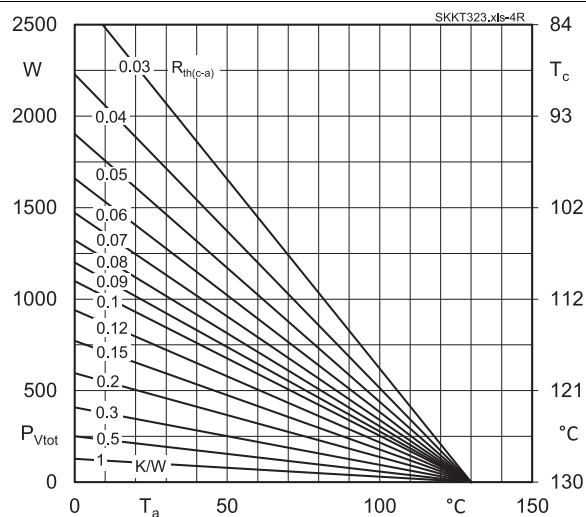


Fig. 4R: Power dissipation of three modules vs. case temperature

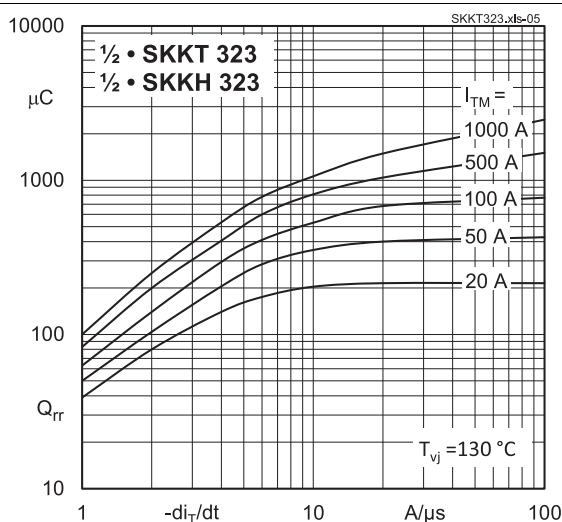


Fig. 5: Recovered charge vs. current decrease

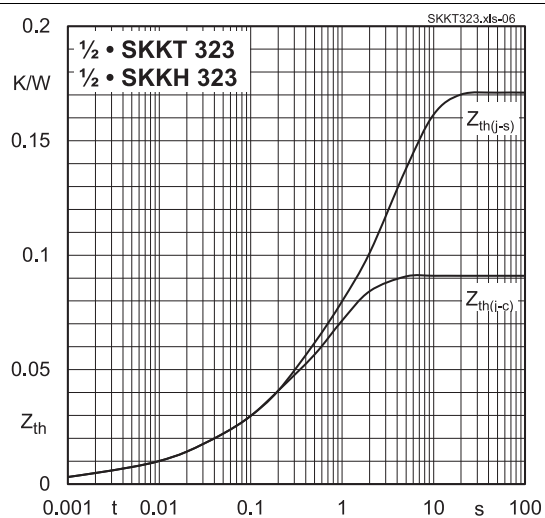


Fig. 6: Transient thermal impedance vs. time

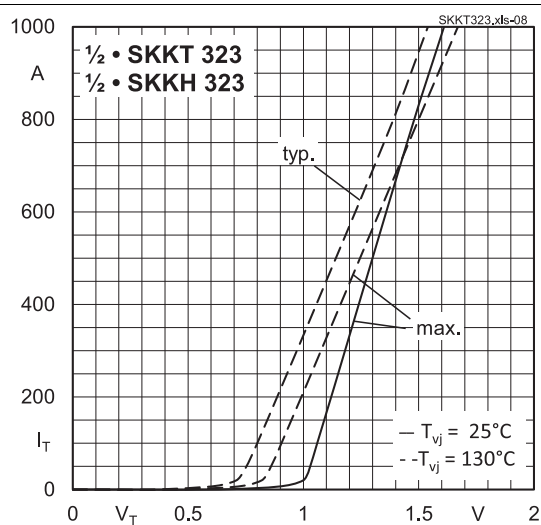


Fig. 7: On-state characteristics

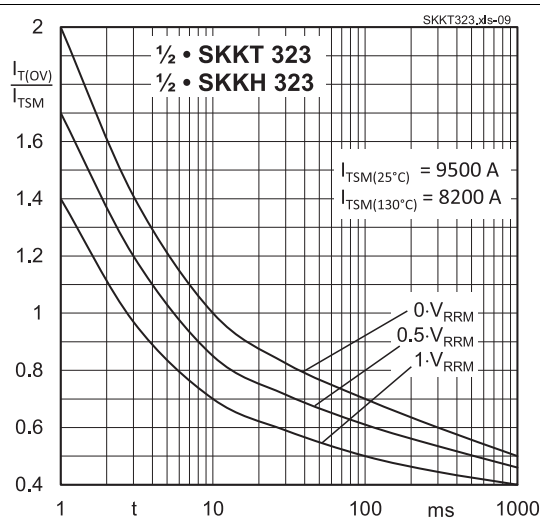


Fig. 8: Surge overload current vs. time

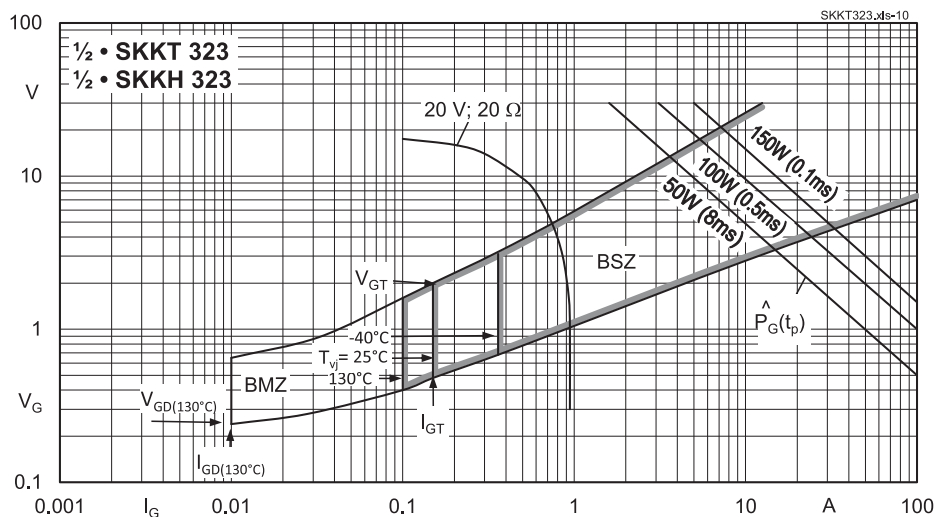
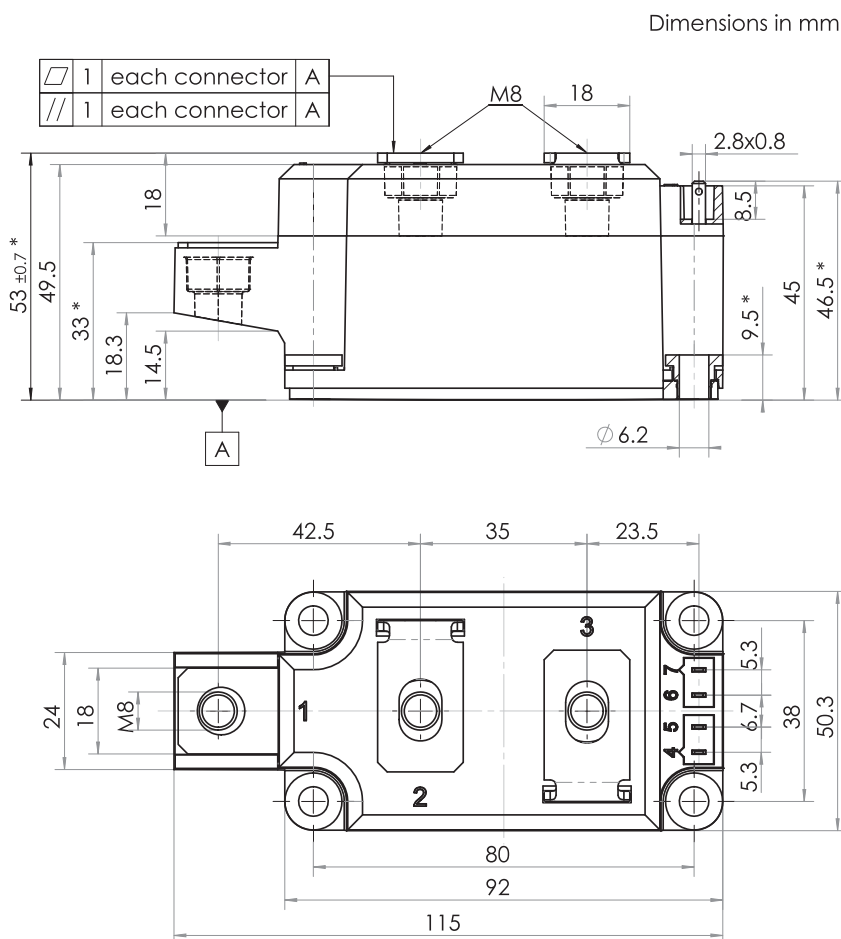
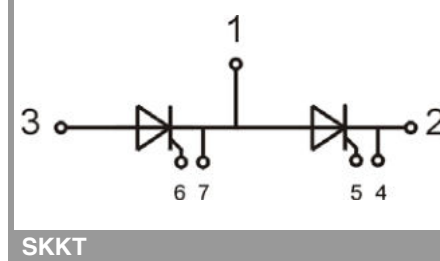


Fig. 9: Gate trigger characteristics



General tolerance  $\pm 0.5$  mm  
\* dimensions valid in assembled condition

## SEMIPACK 3



This is an electrostatic discharge sensitive device (ESDS), international standard IEC 60747-1, chapter IX.

### \*IMPORTANT INFORMATION AND WARNINGS

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